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**Paper Title :** Validation of Load Adaptive Power Efficiency Control in a Processor Power Supply Integrated into Computing System

**Bio :**

Dr. Kawaguchi is a Professor in the Department of Clinical Engineering, Kanagawa Institute of Technology. He received the B.S., and M.S. degrees from the Faculty of Industrial Science and Technology at Tokyo University of Science, Japan, in 1991 and 1993, respectively. He worked at NEC Corporation, Tokyo, Japan, from 1993 to 2019. He received his Ph.D in engineering from Tokyo University of Science in 2015. He joined the faculty at Kanagawa Institute of Technology in 2019. He is currently engaged in research on power supply system of computers.