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Paper Title : Reactive Current Control for Grid-Side Power Factor Correction of WPT System Using Matrix Converter

Bio :

Mr. Yamamoto was born in Hyogo, Japan, in 2000. He received his B.E. and M.E. degrees in electrical and mechanical engineering from Nagoya Institute of Technology, Nagoya, Japan, in 2023 and 2025, respectively. Presently, he is a doctoral course student at Nagoya Institute of Technology, Nagoya, Japan, where he is involved in research on three-phase to single-phase matrix converter.