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Paper Title : Development of Grid-tied Inverters Using Multi-sampling Deadbeat Control

Bio :

Mr. Seki was born in Ibaraki, Japan, in 1993. He received the B.S. and M.S. degrees in School of Science and Technology for Future Life from the Tokyo Denki University, Tokyo, Japan in 2015 and 2017, respectively. Since April 2017, he has been with TMEIC Corporation, Tokyo. His research interests include robust control of power electronics systems using FPGAs.