

ICRERA 2025

**14th INTERNATIONAL CONFERENCE ON RENEWABLE
ENERGY RESEARCH AND APPLICATIONS**

Kouta Tanimoto

National Institute of Technology, Maizuru College



Paper ID : 302

Paper Title : Current Imbalance Reduction for GaN Transistors by Transient Model

Bio:

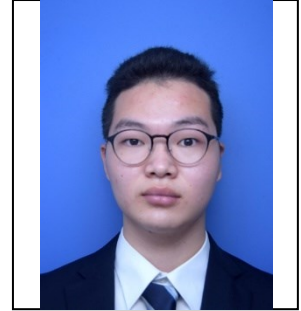
Kouta Tanimoto is a fifth-year student at the Department of Electrical and Computer Engineering, Maizuru National College of Technology, Japan. His research focuses on the stable operation of wide bandgap power devices such as GaN transistors, with an emphasis on suppressing false turn-on and current imbalance phenomena.

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Soichiro Imai

National Institute of Technology Maizuru College



Paper ID : 302

Paper Title : Current Imbalance Reduction for GaN Transistors by Transient Model

Bio :

Soichiro Imai is a fifth-year student in the Department of Electrical and Information Engineering at National Institute of Technology Maizuru College.

His primary research interests' field is the parallelization of GaN.

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Takahiro Nagao

Aisan Industry , Obu, Japan



Paper ID : 302

Paper Title : Current Imbalance Reduction for GaN Transistors by Transient Model

Bio :

Mr. Nagao is Chief Engineer in the Research and Develop division of Aisan Industry. He received Engineering degrees from Nagoya University. His primary research interests include high frequency power electronics and materials for power modules.

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Hideki Uchiki

Aisan Industry , Obu, Japan



Paper ID : 302

Paper Title : Current Imbalance Reduction for GaN Transistors by Transient Model

Bio :

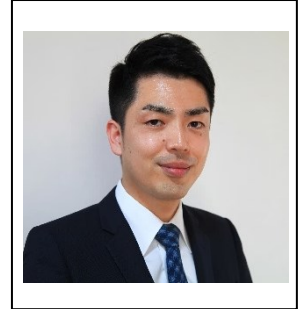
Mr.Uchiki is a manager in the Research and Develop division of Aisan Industry. He received Master and Bachelor of Engineering degrees from Kyoto University. His primary research interests include high frequency power electronics and materials for soft magnetics and power modules and also includes application of statistics for power electronics.

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Assoc. Prof. Dr. Kimihiro Nanamori

National Institute of Technology (KOSEN), Maizuru College, Kyoto, Japan



Paper ID : 302

Paper Title : Current Imbalance Reduction for GaN Transistors by Transient Model

Bio:

Dr. Kimihiro Nanamori is an Associate Professor in the Department of Electrical and Computer Engineering at National Institute of Technology (KOSEN), Maizuru College. He received his Ph.D. degree in Mechanical/Electronic Engineering and Information Systems, his M.S. degree in Mechanical, Electrical and Electronic Engineering, and his B.S. degree in Electronic Control Systems Engineering from Shimane University, Shimane, Japan. His doctoral research focused on ensuring the stable operation of paralleled power devices.

His primary research interests include power device modeling, parallel operation of power devices, and soft-switching DC/AC converters.