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**Paper ID** : 402

**Paper Title** : Effect of DC Bias Characteristics of Multi-layer Ceramic Capacitors on Surge Voltage Suppression in Snubber Circuits

**Bio :**

Prof. Kenji Natori received the B.E. degree in system design engineering and the M. E. and Ph. D. degrees in integrated design engineering from Keio University, Yokohama, Japan, in 2004, 2006, and 2008, respectively. From 2009 to 2012, he was an Assistant Professor at Aoyama Gakuin University, Sagamihara, Japan. Since 2012, he has been with Chiba University, Chiba, Japan and is currently an Associate Professor. His research interests include power electronics and motion control.