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**Paper Title** : EMI Noise Reduction Design for DC-DC Converter Using Zero-Voltage Switching

**Bio :**

Kilho Lee is a Principal Researcher in the EM Environment R&D Department at the Korea Automotive Technology Institute (KATECH) , where he also serves as the Part Leader of the EMC Optimal Design Part. He received his Doctorate Completion in the Department of Future Automobile Engineering from Hanyang University , and his Master's and Bachelor's degrees in Electrical and Electronics Engineering from Pohang University of Science and Technology. His doctoral studies focused on Motor Inverter Electromagnetic Wave EMC , while his master's research focused on Power Electronics / Motor Control.

Mr. Lee previously worked as a Researcher in the Eco-friendly Advanced Design Team at the Hyundai Mobis R&D Center. He also holds an iNarte International Certified EMC Engineer qualification. Currently at KATECH, he is working on optimal design to ensure the EMC reliability of electric vehicle power conversion devices.