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ENERGY RESEARCH AND APPLICATIONS**

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**Paper ID** : 438

**Paper Title** : Active Gate Driver for Current Stress Reduction in Si/SiC Hybrid Switch

**Bio :**

Shunki Hashimoto, 2nd-year Master student. After obtaining a Bachelor of Engineering (B.E.) from the School of Engineering, Nagoya University, currently conducting research in the Power Electronics Laboratory.

The primary research theme is Power Electronics, focusing on research for high-power (high-capacity) applications of power converters.

Specializing in the parallel connection of power devices and proposing methods for current imbalance suppression.